

ANNOUNCEMENTS

THIRD ANNUAL SYMPOSIUM ON LOGIC IN COMPUTER SCIENCE

July 5–8, 1988, Edinburgh, Scotland, U.K.

Concepts and methods from logic are influential throughout computer science. The Annual IEEE Symposium on Logic in Computer Science (LICS) aims to attract broad participation of computer scientists, whose design or research activities involve logic, and logicians interested in computer science. Suggested (but not exclusive) topics of interest include: abstract data types, computer theorem proving, concurrency, data base theory, knowledge representation, finite model theory, lambda and combinatory calculi, logic programming, modal and temporal logics, program logic and semantics, software specification, types and categories, constructive mathematics, verification.

Programme Committee

M. Dezani,	J Mitchell,
Y. Gurevich (chair),	R. Platek,
J. Halpern,	G. Plotkin,
C.A.R. Hoare,	S. Rosenschein,
G. Huet,	P. Sistla,
P. Kanellakis,	J. Tiuryn,
J.-L. Lassez,	M. Wand.

Submission of papers

Send fourteen copies of extended abstract to the programme chairman:

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The package must be airmail postmarked by *27 November 1987* or received by *4 December 1987*. The abstract should be clearly written and provide sufficient detail to allow the programme committee to assess the merits of the paper. References

and comparisons with related work should be included where appropriate. The entire extended abstract should not exceed ten double-spaced pages in 10- or 12-point font. Late abstracts or those departing significantly from these guidelines run a high risk of not being considered. If a copier is not available to the author, a single copy of the abstract will do.

The authors will be notified of acceptance or rejection by 27 January 1988. Accepted papers, typed on special forms for inclusion in the symposium proceedings, will be due 14 March 1988.

The symposium is sponsored by the IEEE Computer Society, Technical Committee on Mathematical Foundations of Computing, and the University of Edinburgh, in cooperation with ACM SIGACT, ASL, and EATCS.

Organizing Committee

J. Barwise, W. Bledsoe, A. Chandra (chair), E. Dijkstra, E. Engeler, J. Goguen, D. Gries, D. Kozen, Z. Manna, A. Meyer, R. Parikh, G. Plotkin, D. Scott

General Chairperson

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WORKSHOP AND SYMPOSIUM ON FORMAL TECHNIQUES IN REAL-TIME AND FAULT-TOLERANT SYSTEMS

September 20–23, 1988, Warwick, England, U.K.

Recent advances in program specification and verification techniques make it possible to consider how similar methods can be developed for systems that must meet real-time constraints and which may need fault-tolerant properties. Ongoing work at various academic and industrial centres suggests that there are significant new developments in topics as widely ranged as providing paradigms for reliable system design, resolving problems of agreement in distributed systems and defining mathematical semantics for real-time programs. The Workshop and Symposium are intended to serve as a focus for such work.

The Workshop will be held from 20–21 September 1988 to provide background lectures and tutorials and will be directed at practising system designers. Admission will be restricted: full details will be available by December 1987.

The Symposium will follow the Workshop, from 22-23 *September 1988*. Papers (upto 20 pages) and extended abstracts are invited for the Symposium on the development and use of formal techniques and mathematical reasoning in real-time and fault-tolerant system design. *Four* copies of each submission should be sent to the Programme Committee Chairman: papers will be refereed and the final choice of papers will be made by the Programme Committee.

Deadlines

- Papers to reach by 15 January 1988
- Final selection by 31 March 1988
- Camera-ready copy by 15 May 1988

Programme Committee

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Sponsorship

The Workshop and Symposium are being organized by the Department of Computer Science, University of Warwick, with generous contributions from GEC Telecommunications.